

LIGHTING RELAY SCHEDULE LCPIN

Table with 4 columns: RELAY, CIRCUITS, LOAD/CONTROLLED, REMARKS. Lists relays 1-6 for fluorescent types LFN-D through LFN-L.

LCAD QR-40x327 with TC, pendant connector and BIM sign

PANEL P2NC (1)

Panel schedule table for PANEL P2NC (1) showing mounting surface, volt-amps, descriptions, and values.

10000 ALC, 225A BIS. TOTAL DISIGN VA: 54, TOTAL DISIGN AMPS: 150

PANEL P2NC (2)

Panel schedule table for PANEL P2NC (2) showing mounting surface, volt-amps, descriptions, and values.

10000 ALC, 225A BIS. TOTAL DISIGN VA: 31, TOTAL DISIGN AMPS: 87

PANEL P2NA (1)

Panel schedule table for PANEL P2NA (1) showing mounting surface, volt-amps, descriptions, and values.

10000 ALC, 225A BIS. TOTAL DISIGN VA: 62, TOTAL DISIGN AMPS: 172

PANEL P2NA (2)

Panel schedule table for PANEL P2NA (2) showing mounting surface, volt-amps, descriptions, and values.

10000 ALC, 225A BIS. TOTAL DISIGN VA: 23, TOTAL DISIGN AMPS: 78

PANEL P2NB (1)

Panel schedule table for PANEL P2NB (1) showing mounting surface, volt-amps, descriptions, and values.

10000 ALC, 225A BIS. TOTAL DISIGN VA: 63, TOTAL DISIGN AMPS: 175

PANEL P2NB (2)

Panel schedule table for PANEL P2NB (2) showing mounting surface, volt-amps, descriptions, and values.

10000 ALC, 225A BIS. TOTAL DISIGN VA: 27, TOTAL DISIGN AMPS: 78

PANEL P1NA (1)

Panel schedule table for PANEL P1NA (1) showing mounting surface, volt-amps, descriptions, and values.

10000 ALC, 225A BIS. TOTAL DISIGN VA: 31, TOTAL DISIGN AMPS: 142

PANEL P1NA (2)

Panel schedule table for PANEL P1NA (2) showing mounting surface, volt-amps, descriptions, and values.

10000 ALC, 225A BIS. TOTAL DISIGN VA: 24, TOTAL DISIGN AMPS: 67

PANEL P1NB (1)

Panel schedule table for PANEL P1NB (1) showing mounting surface, volt-amps, descriptions, and values.

10000 ALC, 225A BIS. TOTAL DISIGN VA: 40, TOTAL DISIGN AMPS: 111

PANEL P1NB (2)

Panel schedule table for PANEL P1NB (2) showing mounting surface, volt-amps, descriptions, and values.

10000 ALC, 225A BIS. TOTAL DISIGN VA: 24, TOTAL DISIGN AMPS: 65

PANEL L1N (1)

Panel schedule table for PANEL L1N (1) showing mounting surface, volt-amps, descriptions, and values.

22000 ALC, 100A BIS. TOTAL DISIGN VA: 39, TOTAL DISIGN AMPS: 48

PANEL L2N

Panel schedule table for PANEL L2N showing mounting surface, volt-amps, descriptions, and values.

22000 ALC, 100A BIS. TOTAL DISIGN VA: 22, TOTAL DISIGN AMPS: 28

DISTRIBUTION BOARD "DPM1"

Table showing kVA and Amps for various loads including AHU-2/5A, AHU-2/6A, and Fan-1.

DISTRIBUTION BOARD "LDPM1"

Table showing kVA and Amps for various loads including PANEL P1NA (1) and PANEL P1NA (2).

Logos and contact information for WRNS, Steinberg Architects, Hensel Phelps Construction Co., DECKER ELECTRIC, and RANDALL LAMB.

PROJECT INFORMATION block containing details like PROJECT NO: SKY0100, DATE: 04/26/08, and SHEET TITLE: PANEL SCHEDULE.

RECORD DIMENSIONS block with DIVISION OF PLUMBING and DATE 01-11-01.

REVISIONS block with a table showing changes and dates.

DISTRICT OF COLUMBIA PROJECT INFORMATION block with project name: SKYLINE COLLEGE CIP2 DESIGN-BUILD BUILDING 4 INCREMENT 3.