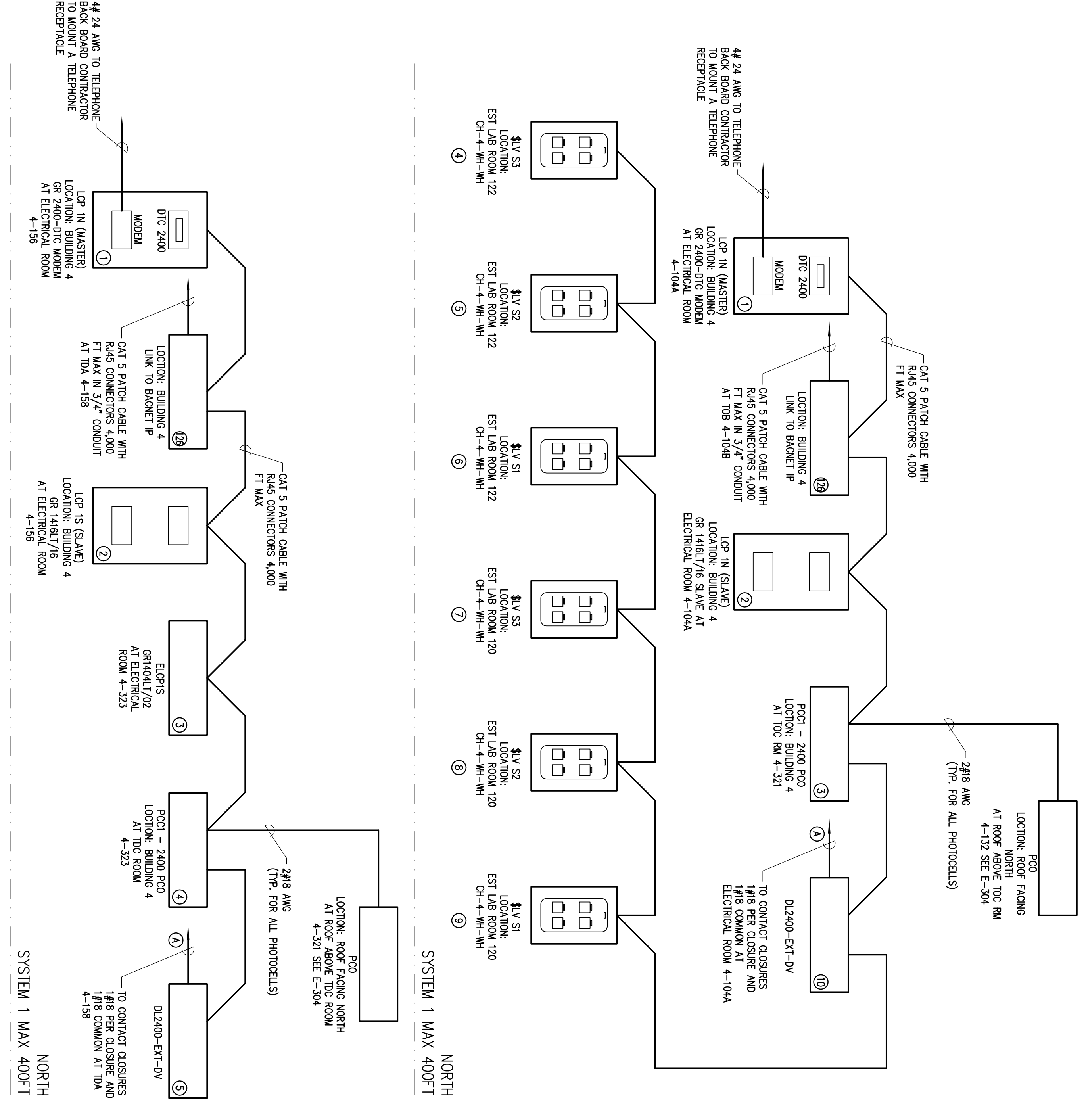


NO.	REVISIONS	DATE
1001	SCHEMATIC DESIGN	04/2008
1002	50% DESIGN DEVELOPMENT	06/13/08
1003	SCHEMATIC DEVELOPMENT	07/20/08
1004	50% DESIGN DEVELOPMENT	07/20/08
1005	DATA REVIEW	10/10/08
1006	DATA REVIEW	11/17/08
1007	DATA REVIEW	11/11/08
1008	DATA REVIEW	11/11/08
1009	DATA REVIEW	12/22/08
1010	DATA REVIEW	02/03/09
1011	DATA REVIEW	04/08/09
1012	DATA REVIEW	05/12/09
1013	DATA REVIEW	06/10/09
1014	DATA REVIEW	07/01/09
1015	DATA REVIEW	07/27/10

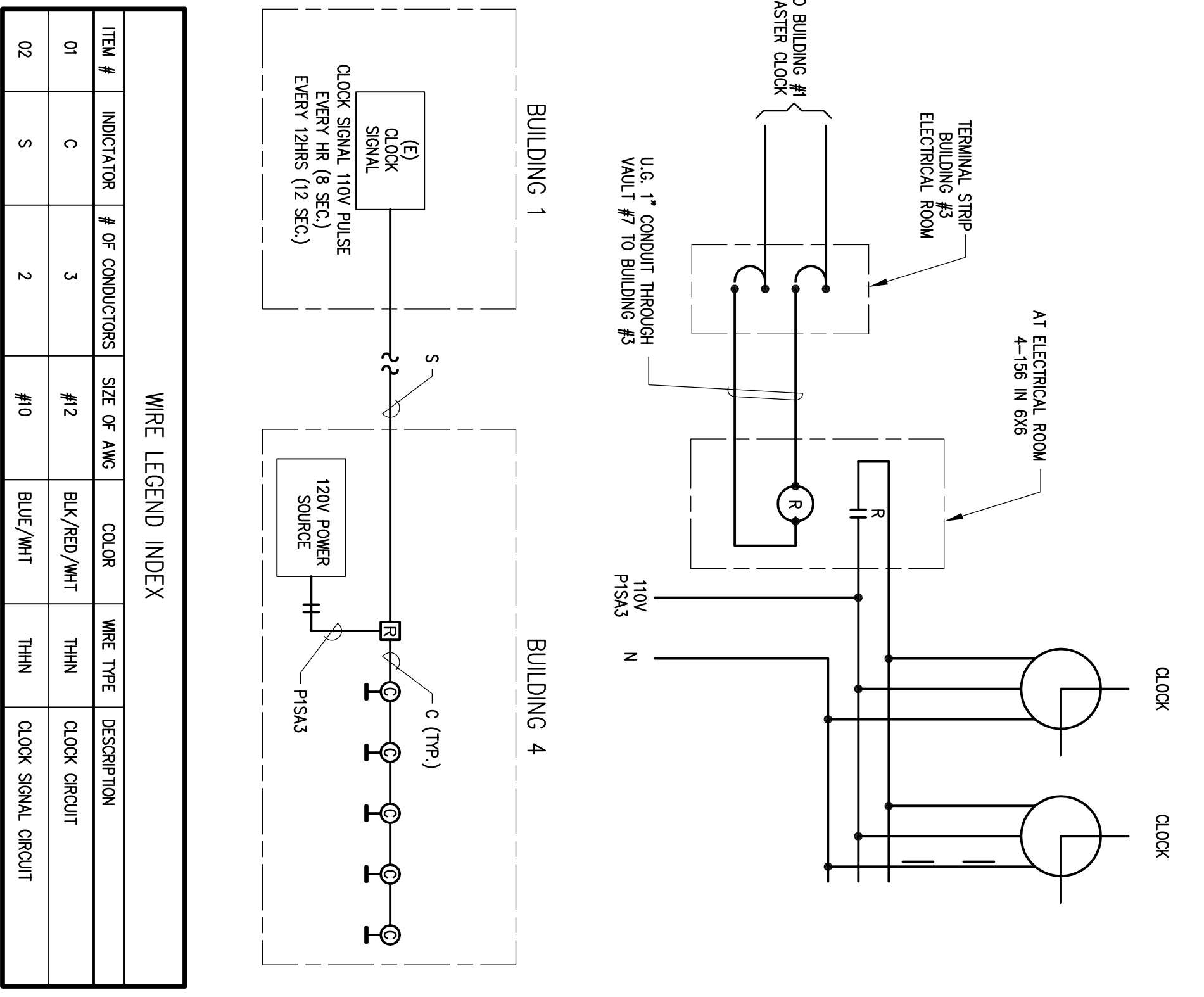
NO. 01513 ON OF 1411 VISITING ARCHITECT
 APR # 01-110117
 AC _____ RS _____
 SSS _____ DATE _____
Increment 3
 IDENTIFICATION STAMP

SKYLINE COLLEGE
 SAN MATEO COUNTY
 DISTRICT
CIP2 DESIGN-BUILD
PROJECT
BUILDING 4
INCREMENT 3

SHEET TITLE:
RISER DIAGRAMS
 PROJECT NO: SP1057010
 DRAWN BY: JB
 DATE: 04/28/08
 CHECKED BY: ALS
 SCALE: NONE



LIGHTING CONTROL WIRING DIAGRAM
 SCALE: NONE **3**

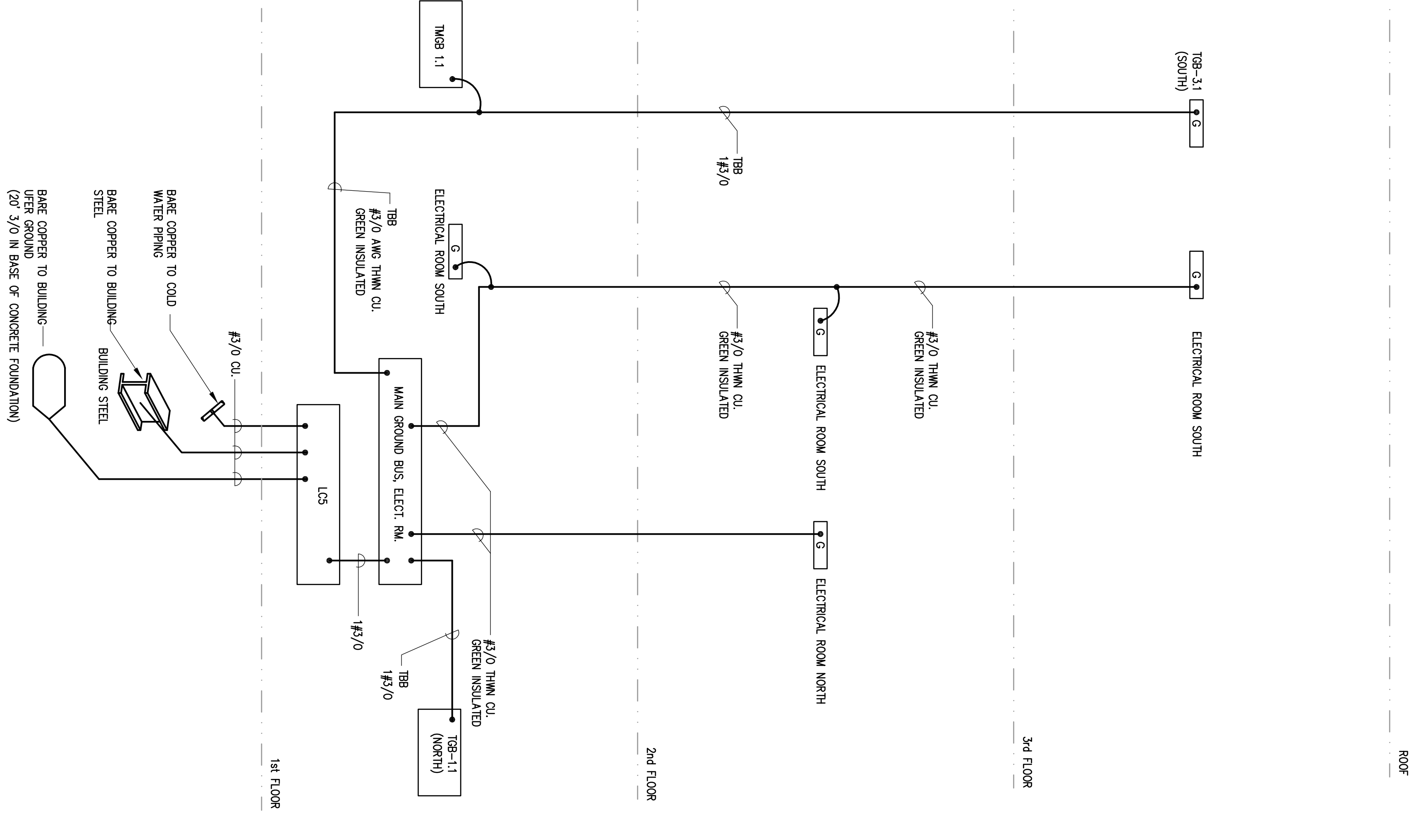


WIRE LEGEND INDEX

ITEM #	INDICATOR	# OF CONDUCTORS	SIZE OF AWG	COLOR	WIRE TYPE	DESCRIPTION
01	C	3	#12	BLK/YEL/WHI	THIN	CLOCK CIRCUIT
02	S	2	#10	BLUE/WHI	THIN	CLOCK SIGNAL CIRCUIT

WIRE NOTES:
 1. MAINTAIN 40% CONDUIT FILL
 2. OUTDOOR WIRING SHALL BE USED FOR NET LOCATIONS
 3. CONDUITS ROUTED UNDERGROUND OR IN EXTERIOR LOCATIONS SHALL BE WATER TIGHT FITTINGS.

CLOCK SYSTEM RISER DIAGRAM
 SCALE: NONE **2**



GROUNDING SYSTEM RISER DIAGRAM
 SCALE: NONE **1**

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